

## UNITED STATES PATENT AND TRADEMARK OFFICE

## In the **PATENT APPLICATION** of:

Foore, et. al

**Application No.:** 10/767,326

**Confirmation No.:** 5495

Filed:

January 29, 2004

For: DYNAMIC FRAME SIZE ADJUSTMENT AND SELECTIVE REJECT ON A MULTI-LINK CHANNEL TO IMPROVE EFFECTIVE THROUGHPUT AND BIT ERROR RATE

Group:

2616

Examiner:

Asfar M. Qureshi

Our File:

TAN-2-1400.05.US

Date:

October 11, 2007

## REPLY PURSUANT TO 37 C.F.R. §1.114

Mail Stop RCE Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

This Reply is being timely filed in response to the Notice of Allowance dated July 19, 2007. A Request for Continued Examination (RCE) is filed concurrently herewith.

Please amend the application without prejudice or disclaimer as follows: